

09/382402

# Block Diagram

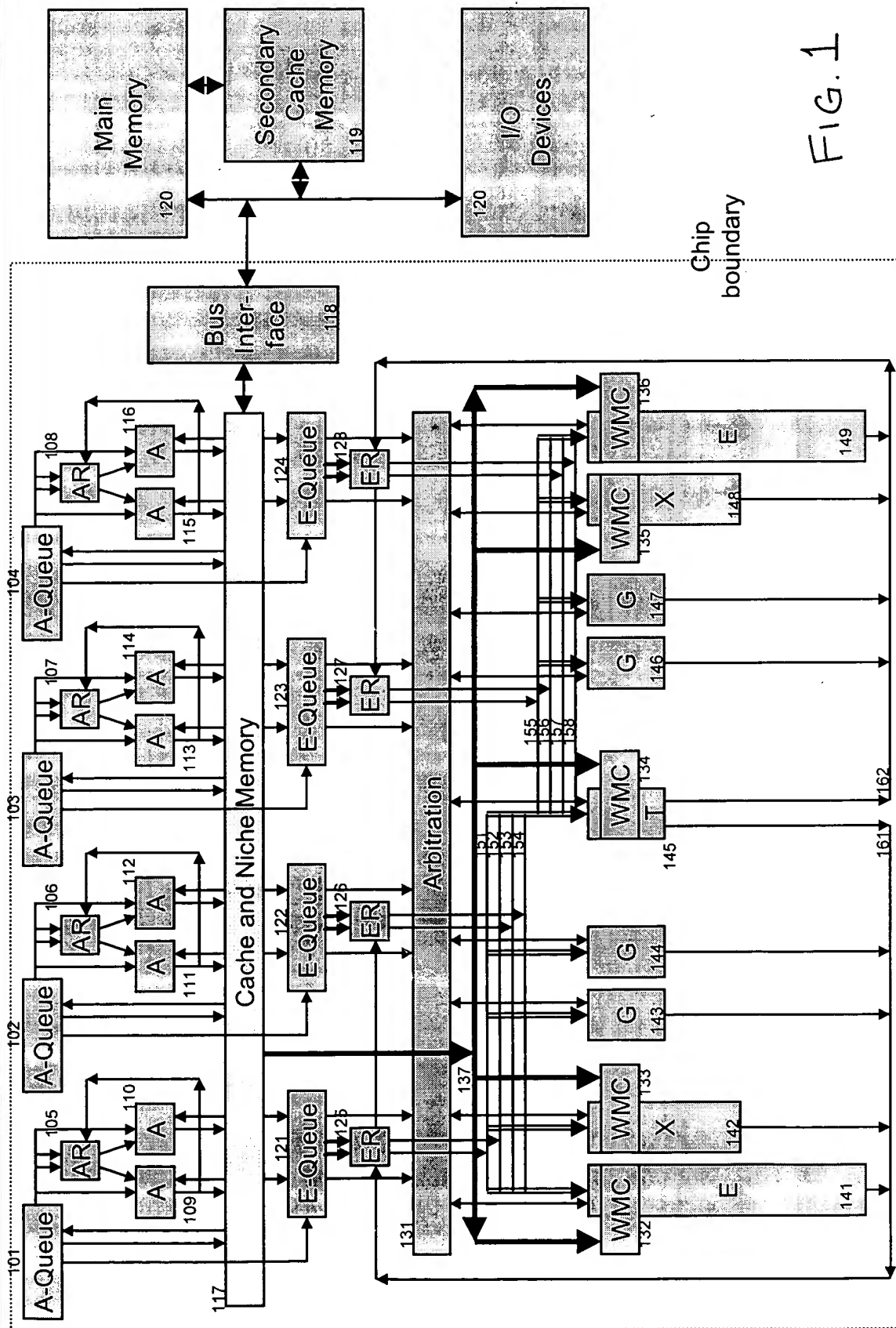


FIG. 1

# wide multiply matrix

$$\blacksquare \text{rd}_{128} = \text{m}[\text{rc}]_{(128 \times 64/\text{size})} * \text{rb}_{128}$$

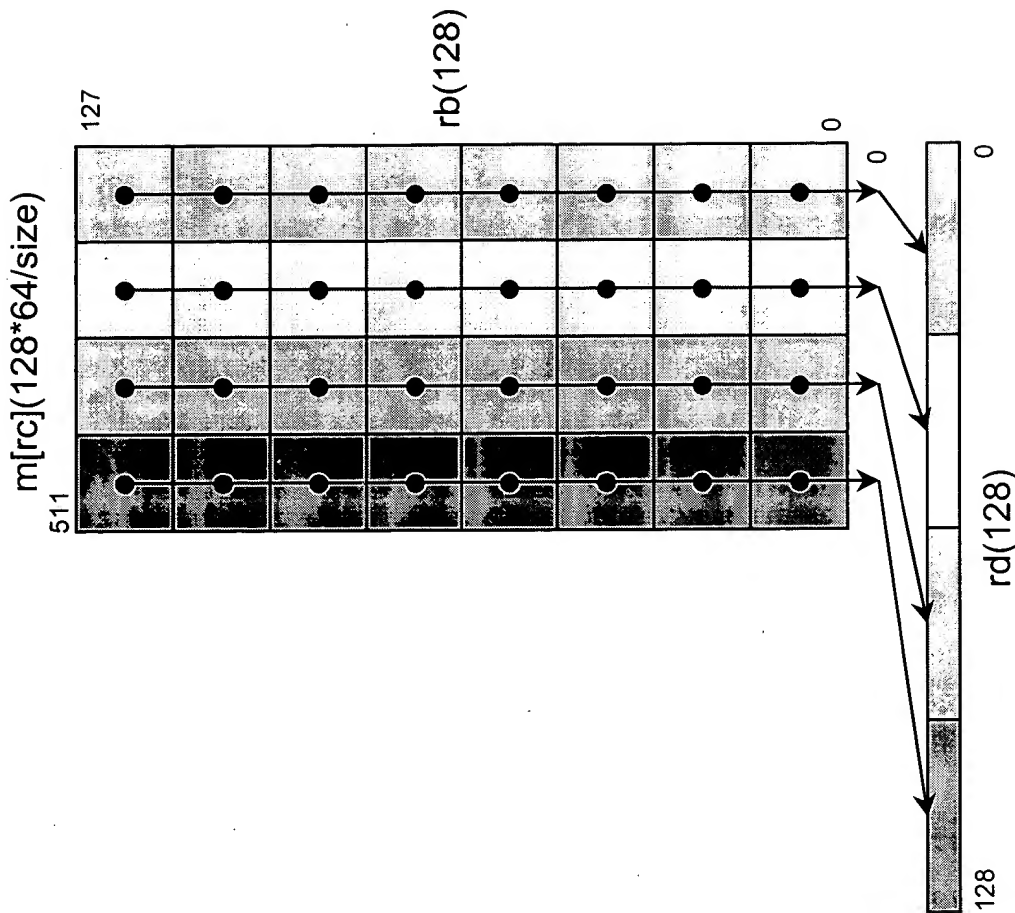


FIG. 2

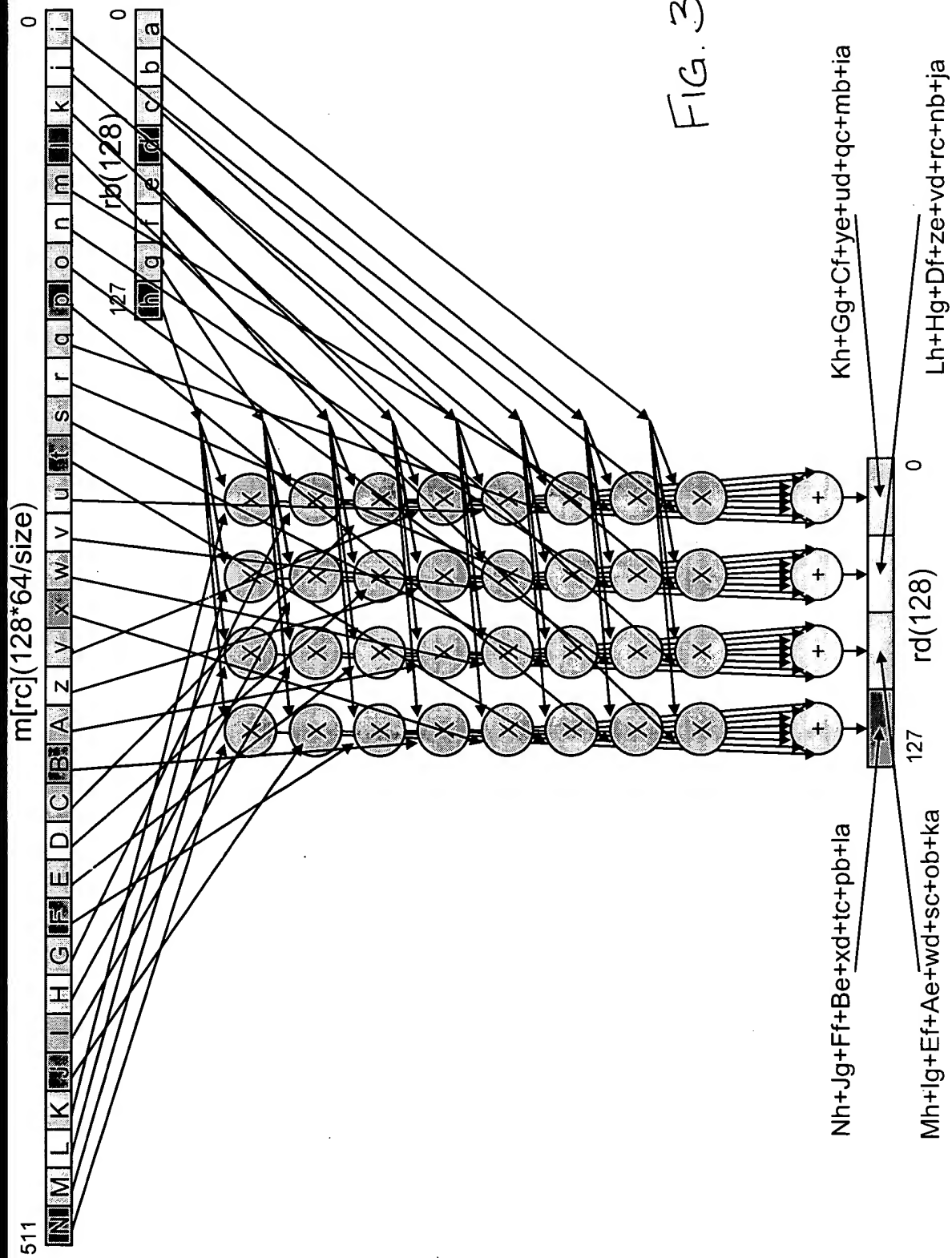
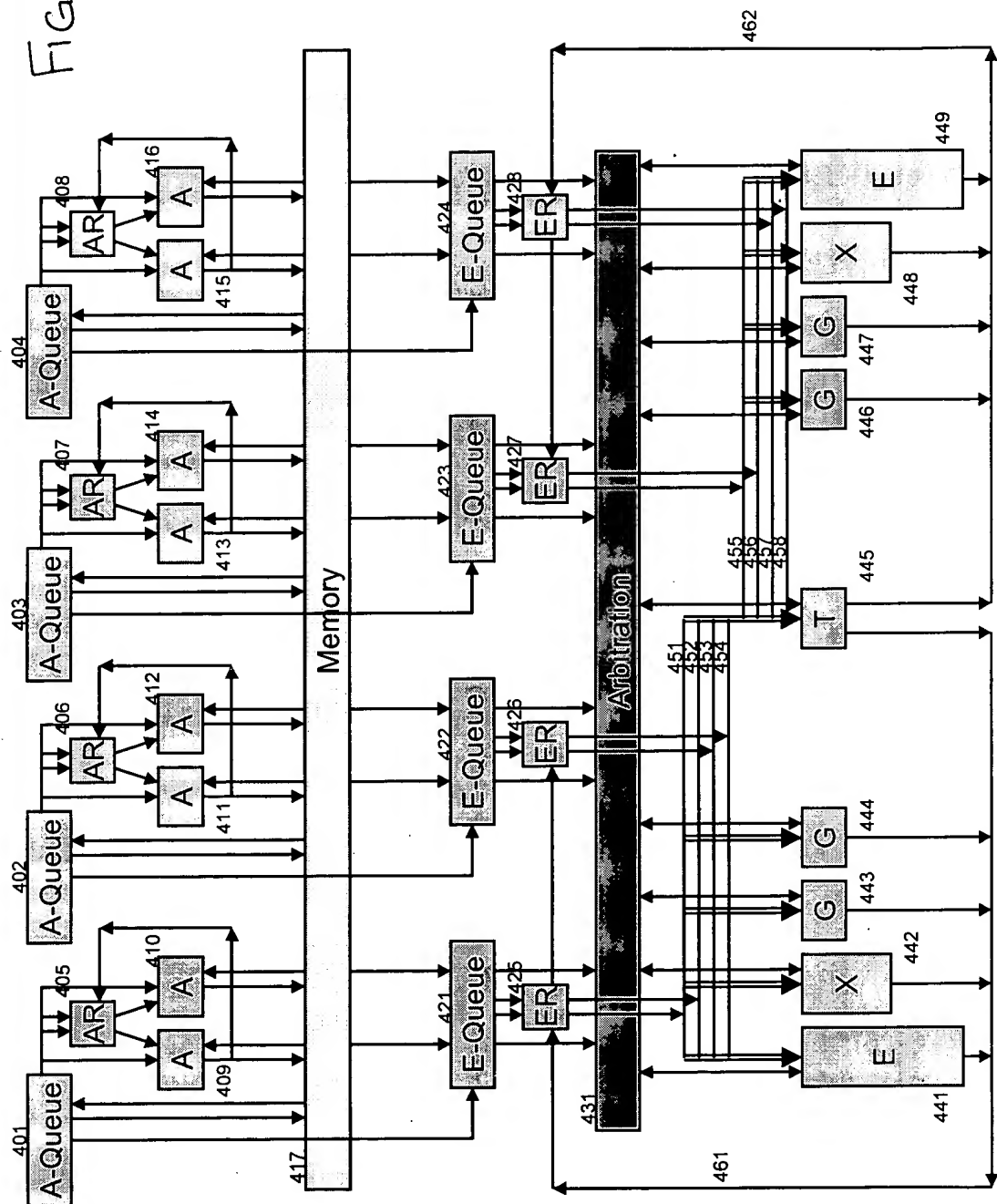


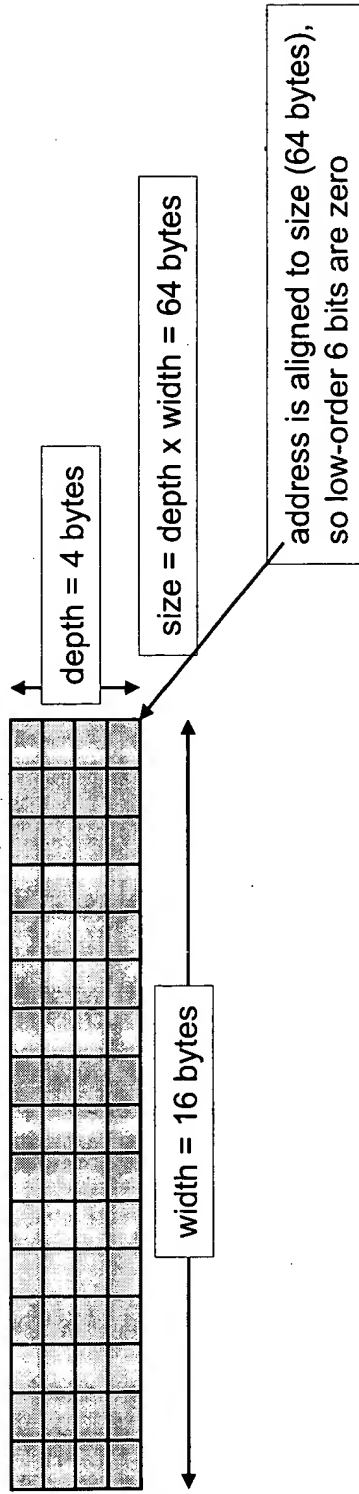
Fig. 4



# Wide operand specifier

■ specifier=address+(size/2)+(width/2)

FIG. 5



address  
aaaaaaaaaaaaaaaaaaaaaaaaaaaa

000000

size/2  
00000000000000000000000000000000

100000

width/2  
00000000000000000000000000000000

001000

specifier  
aaaaaaaaaaaaaaaaaaaaaaaaaaaa

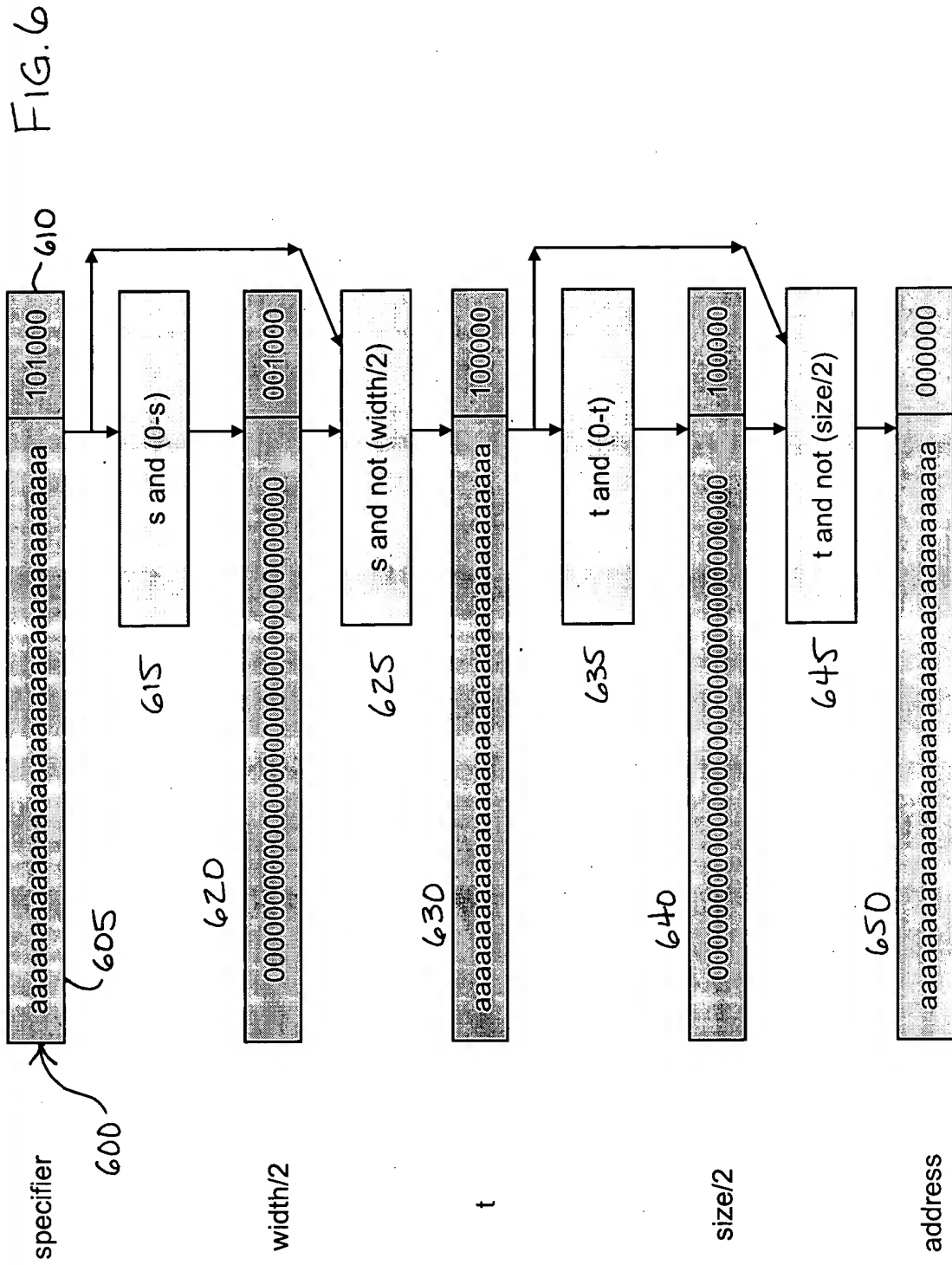
101000

500 →

← 505

510

# Software Decoding



# Wide function unit

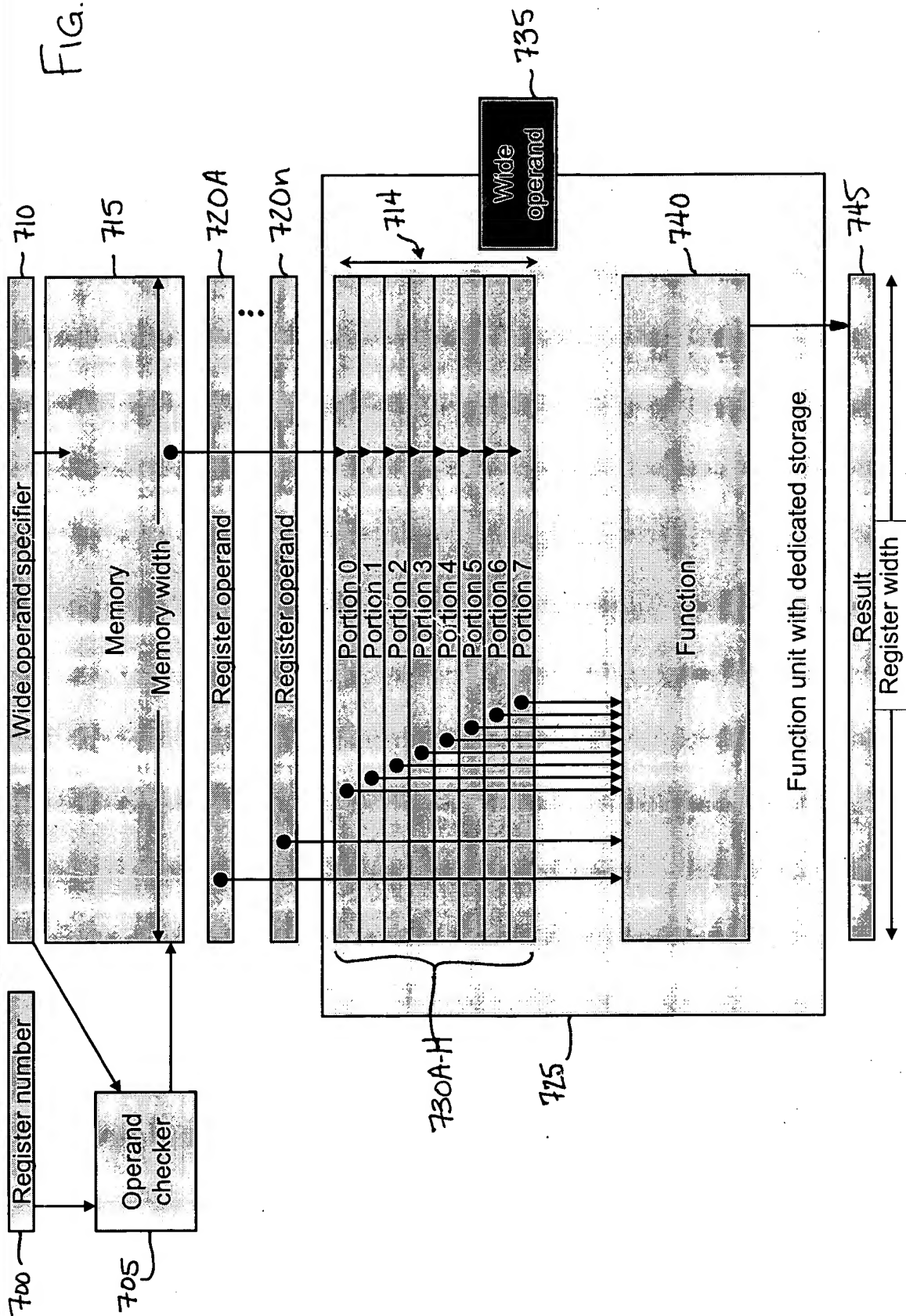
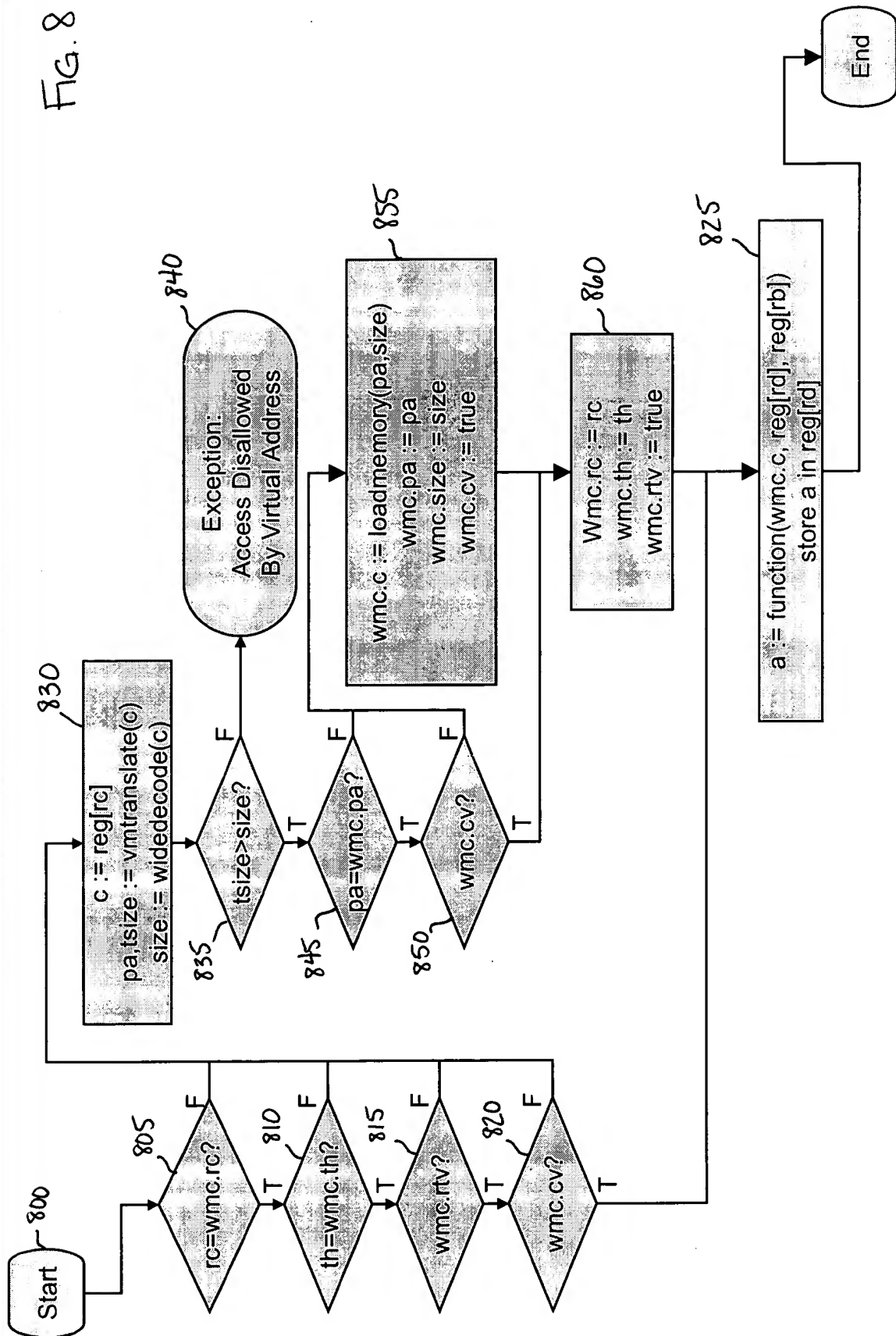


FIG. 8







thread number

